

## CLAIMS

1. A method for downloading software to a device, comprising:  
connecting a source of software to a universal serial bus (USB) port communicating with the device;  
downloading the software from the source to the device; and  
resetting the device by undertaking at least one of: resetting registers in a processor of the device except for USB-associated registers; maintaining a USB transceiver associated with the device in an "on" state; resetting values in a RAM associated with the device except for USB-associated values; and setting a flash memory associated with the device in a data mode.
2. The method of Claim 1, wherein prior to said resetting act, the USB-associated registers of the processor are configured with non-default values, and the method further comprises:  
resetting all registers in the processor except for the USB-associated registers; and  
maintaining the non-default values in the USB-associated registers during the resetting act.
3. The method of Claim 1, further comprising maintaining the USB transceiver in the "on" state during the resetting act, wherein the USB transceiver communicates with the source of software through the USB port.
4. The method of Claim 1, wherein prior to said resetting act, the USB-associated values in the RAM have non-default values, and the method further comprises:  
resetting RAM values to default values except for the USB-associated values; and  
maintaining the USB-associated values during the resetting act.
5. The method of Claim 1, wherein the resetting act comprises all of: resetting all registers in a processor of the computer except for USB-associated registers; maintaining a USB transceiver associated with the computer in an "on" state; resetting values in a RAM associated with the computer except for any USB-associated values; and setting a flash memory associated with the computer in a data mode.

6. The method of Claim 1, wherein the device is a CDMA wireless device.
7. A wireless communication device, comprising:
  - at least one processor having registers, at least some of which are USB-associated registers containing pre-reset values;
  - at least one USB transceiver configured for communicating with the USB-associated registers of the processor and with a source of software external to the wireless communication device; and
  - logic executable by the processor for resetting the wireless communication device under at least one reset condition by:
    - resetting registers other than the USB-associated registers to their default value,
    - maintaining the pre-reset values in the USB-associated registers at least while the non-USB-associated registers are reset, and
    - maintaining the USB transceiver energized during the act of resetting the wireless communication device.
8. The wireless communication device of Claim 7, wherein the reset condition is related to downloading of software from the source of software.
9. The wireless communication device of Claim 7, further comprising at least one RAM communicating with the processor, the logic causing the processor during the resetting act to reset some RAM values to default values while maintaining USB-related non-default values.
10. The wireless communication device of Claim 7, further comprising flash memory communicating with the processor, the logic causing the processor to configure the flash memory in a data mode during the act of resetting.
11. The wireless communication device of Claim 7, wherein the wireless communication device is a CDMA device.

12. A system, comprising:  
a source of software;  
a universal serial bus (USB) port connected to the source of software;  
a wireless communication device supporting the USB port, the wireless communication device including at least one processor and at least one USB transceiver interconnecting the processor and the USB port; and  
means for resetting all registers in the processor except for registers associated with the USB transceiver when a reset condition is satisfied.

13. The system of Claim 12, further comprising:  
means for maintaining the USB transceiver in an "on" state during execution of the means for resetting.

14. The system of Claim 12, further comprising:  
at least one RAM communicating with the processor; and  
means for resetting values in the RAM except for USB-associated values, during the execution of means for resetting all registers.

15. The system of Claim 12, further comprising:  
at least one memory communicating with the processor; and  
means for maintaining the memory in a normal mode, during the execution of means for resetting all registers.

16. The system of Claim 12, further comprising a USB clock source associated with the device, and means for maintaining the USB clock source.

17. An apparatus for downloading software to a device, comprising:  
means for connecting a source of software to a universal serial bus (USB) port communicating with the device;  
means for downloading the software from the source to the device; and  
means for resetting the device by undertaking at least one of: resetting registers in a processor of the device except for USB-associated registers; maintaining a USB transceiver

associated with the device in an "on" state; resetting values in a RAM associated with the device except for USB-associated values; and setting a flash memory associated with the device in a data mode.